

## Job Title

### SoC Digital Design Engineer (Junior)

Safe-IoT (<https://safe-iot-ic.com>) is a fabless semiconductor company designing, developing and marketing ultra-low power and secure wireless systems-on-chip for IoT. We innovate to integrate state-of-the-art Hardware security and Ultra-low power in order to achieve lowest BOM cost to address 5G and hybrid IoT networks.

## Job description

As part of the SoC Digital Design Team, you will participate to the development of System on Chip (SoC) products to address the IoT market.

You will be responsible for the RTL design, simulation, optimization, integration and documentation of digital blocks in the SOC.

You will perform the design verification, either at IP level or at chip level, using advanced verification techniques and tools, including development of embedded software for 32-bit RISC processors.

You will work with other functional teams during the development phase and participate to the debug of the device on demonstration boards using various lab tools.

The position is located at Rousset, France.

## Qualifications

Master's degree and/or Engineering school specialized in microelectronics design.

Junior/New college graduate/post-PhD accepted.

First experience as design engineer is appreciated but is not mandatory.

## Skills

- Curious, creative, autonomous, good sense of teamwork.
- RTL design and verification techniques (Verilog).
- Embedded software development (C, assembler).
- Scripting and automation languages (Python, MATLAB, LabVIEW, ...).
- Microcontroller architectures (ARM, RISC-V), communication protocols and cryptography.
- Good communication in both French & English.

Please, apply at [talents@safe-iot-ic.com](mailto:talents@safe-iot-ic.com)